(43) Date of A Publication 24.09.2003

- (21) Application No 0206737.9
- (22) Date of Filing 22.03.2002
- (71) Applicant(s)

Motorola Inc (Incorporated in USA - Delaware) Corporate Offices, 1303 East Algonquin Road, Schaumburg, Illinois 60196, United States of America

(72) Inventor(s)

Christopher Temple Mathias Rausch

(74) Agent and/or Address for Service

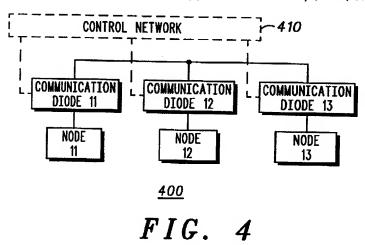
Susan Potts Motorola Limited, **European Intellectual Property Section,** Law Department, Midpoint, Alencon Link, BASINGSTOKE, Hampshire, RG21 7PL, **United Kingdom**

- (51) INT CL7 H04L 12/403
- (52) UK CL (Edition V) H4P PPK
- (56) Documents Cited WO 2002/047334 A1 US 4530085 A
- (58) Field of Search UK CL (Edition T) H4P PPD PPG PPHB PPJB PPK INT CL7 G06F 11/20, H04L 12/40 12/403 12/407 12/42 12/423 12/427 12/44 29/14 Other:

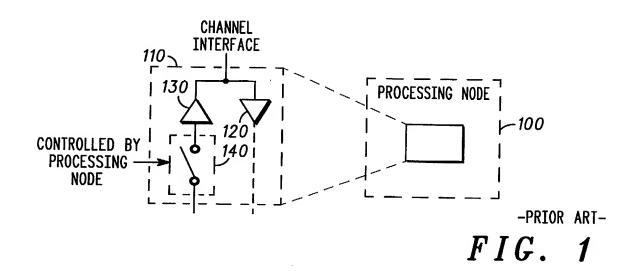
(54) Abstract Title

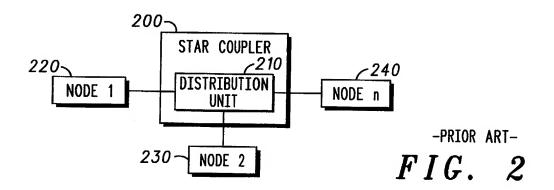
Communications network node access switches

(57) A communications network 400 and an arrangement to control access to the network nodes N11 to N13 by the use of externally controlled switches D11 to D13. The control 410 of the switches may be in the time domain, or embedded in frames and are used to enable/disable signals onto the input/output channels. The switches can be used to protect a distributed computer system such as a bus or star network from node failure or to protect subnetworks from other failures within another subnetwork. The switches are placed in strategic positions and can serve as a firewall, or to provide resilience against spatial proximity faults by allowing the enforcement of fail silence in the time domain. This allows for the isolation of a faulty processing node within an embedded system such as for automotive by-wire applications (FlexRay (RTM) and TTP (RTM)).



2 386 804





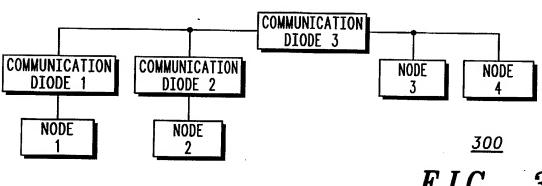
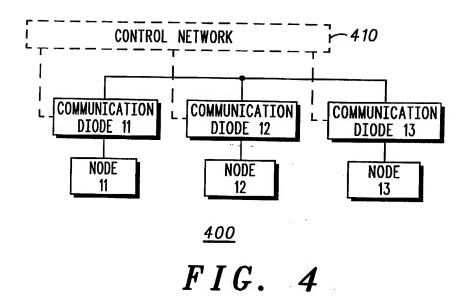


FIG. 3



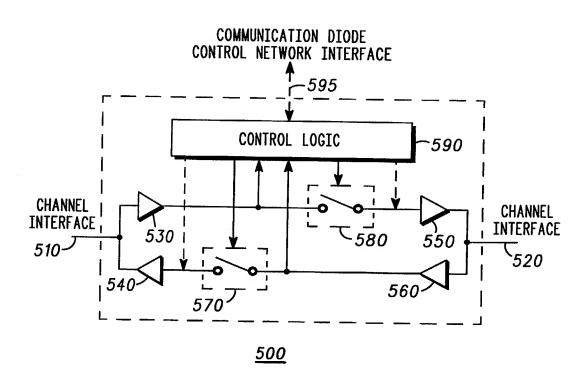


FIG. 5

COMMUNICATION NETWORK AND ARRANGEMENT FOR USE THEREIN

5 Field of the Invention

This invention relates to communication networks and particularly, though not exclusively, to embedded, fault-tolerant, dependable, distributed computer systems.

10

30

Background of the Invention

In a distributed physical real-time system based on shared communication media, such as a broadcast bus or star topology, it is important to prevent a single faulty node from monopolizing the communication media. Since it cannot be assumed that a faulty node obeys the system's media arbitration policy, but that it will rather send messages at arbitrary points in time, it is necessary to protect the communication media against such uncontrolled node failures.

Two approaches are known for protecting shared

25 communication media against uncontrolled node failures.

Both approaches assume a regular, temporal deterministic media access scheme:

 A first approach proposes integrating a device, called bus guardian, with each node that controls the node's access to the communication media. The bus guardian is provided with a priori information about the transmission scheme of its associated node and enforces that access to the communication media is only given in accordance with the transmission scheme.

• A second approach is based on a star topology. It proposes integrating a distribution unit within the star coupler granting access of the respective nodes to the star according to a cyclical time slice method. Again the distribution unit, which is provided a priori before run-time with the transmission scheme of the connected nodes, imposes that at a given instant only one node is capable of transmitting to the remaining nodes according to the transmission scheme.

15

20

25

From US patent no. 4,015,246, titled "Synchronous Fault Tolerant Multi-Processor System", there is known a bus guardian for a non-distributed fully synchronous multi-processing system based on very specific architectural assumptions (minimum 3 buses, 6 processors).

From US patent no. 4,860,280, titled "Apparatus and Method for a sSecure and Diagnosable Antijabber Communication Circuit", it is known that in order to prevent 'jabber' (the uncontrolled transmission of messages on a communication channel) an anitjabber timing unit is frequently used to determine whether a message on the communication channel exceeds the maximum predetermined length of time.

From the publication of the Institut für Technische Informatik, Technische Universitat Wien, titled "Avoiding the Babbling-Idiot Failure in a Time-Triggered Communication System", it is known to use a bus guardian added to each node to protect a communication bus from babbling-idiot failure by exploiting the regular transmission poattern of a time-triggered system in order to enforce fail-silent behaviour of the node in the time domain.

10

From patent publication WO 0113230 A1, 2001, titled "Method for Imposing the Fail-Silent Characteristic in a Distributed Computer System and Distribution Unit in such a System", it is known to use a server-interconnecting distribution unit which knows a priori the servers' regular transmission pattern and imposes that a server is only able to transmit to remaining servers within a statically allocated time slice.

- 20 From US patent no. 5,355,375, titled "Hub Controller for Providing Deterministic Access to CSMA Local Area Network", it is known to alter a basic non-deterministic contention algorithm of the CSMA/CD protocol LAN within a hub controller to inhibit any CSMA/CD transmissions by a port, allowing the hub controller to control which of the multiple ports will be allowed to contend for access to a common internal bus within the hub controller and for how long.
- 30 It will be understood that these known techniques fall into one of the two approaches summarized above.

However, both approaches suffer from drawbacks:

- The first approach suffers as it relies on functional independence between the node and its associated bus guardian in the event of a fault, since perceivable faults may cause not only the node but also its associated bus guardian to fail in an uncontrolled way. Due to the physical proximity of the two units this independency cannot always be convincingly ensured.
- The second approach suffers from use of a star coupler. In many environments it is not feasible to run a communication channel from every node to the star coupler for economical reasons. In addition, the star coupler represents a single point of failure in the system that has a higher probability of failure compared to a passive component such as a bus as it contains a significant number of active components, such as, for example, a microcontroller.

20

15

5

10

A need therefore exists for a communication network and arrangement for use therein wherein the abovementioned disadvantage(s) may be alleviated.

25

Statement of Invention

In accordance with a first aspect of the present invention there is provided a communication network as claimed in claim 1.

In accordance with a second aspect of the present invention there is provided an arrangement for use in a communication network as claimed in claim 9.

In brief, the invention proposes introducing an arrangement or component into a network, such as a distributed system, the component operating as a "communication diode". This component is placed at strategic positions within the communication network, where it serves as a firewall for uncontrolled node failures. This allows the enforcement of fail-silence in the time domain within a distributed computer systems showing resilience against spatial proximity faults.

15

20

25

Brief Description of the Drawings

One communication network and arrangement for use therein incorporating the present invention will now be described, by way of example only, with reference to the accompanying drawing(s), in which:

FIG. 1 shows a schematic block-diagram illustrating a known guard device located within and controlled by a processing node in a prior art communication network:

FIG. 2 shows a schematic block-diagram illustrating a known guard device located within and controlled by a central distribution unit in a prior art communication network;

FIG. 3 shows a schematic block-diagram illustrating the structure of a novel communication diode for use in a communication network incorporating the invention; and

FIG. 4 shows a schematic block-diagram illustrating a communication network containing four nodes and three communication diodes, as shown in FIG. 3, incorporating the invention; and

FIG. 5 shows a schematic block-diagram illustrating a communication network containing three nodes and three communication diodes, as shown in FIG. 3, incorporating the invention.

Description of Preferred Embodiment

5

10

15

- In brief, the invention in one aspect introduces an arrangement or component into a distributed system that operates as a "communication diode". This component is placed at one or more strategic positions within the communication network, where it serves as a firewall for uncontrolled node failures.
 - FIG. 1 shows a first, known prior art approach in which each node 100 includes a device 110, typically termed a bus guardian, that controls the node's access to the communication media. The bus guardian 110 contains input and output amplifiers 120 and 130; the bus guardian 110

also contains a switch 140, controlled by the processing node, which enables or disables the node for outputting signals to a channel interface (and thereby onto the communication media). The bus guardian 110 is provided with *a priori* information about the transmission scheme of its associated node and enforces that access to the communication media is only given in accordance with the transmission scheme.

5

30

transmission scheme.

- It will be appreciated that the approach illustrated in FIG. 1 suffers as it relies on functional independence between the node and its associated bus guardian in the event of a fault, since perceivable faults may cause not only the node but also its associated bus guardian to fail in an uncontrolled way, and due to the physical proximity of the two units this independency cannot always be convincingly ensured.
- FIG. 2 shows a second known, prior art approach which is based on a star topology. A star coupler 200 has integrated within it a distribution unit 210 which grants access of the respective nodes 1-n (of which only three, node 220, node 230 and node 240 are shown) to the star coupler 200 according to a cyclical time slice method.
 Similarly to the approach of FIG. 1 discussed above, the distribution unit 210 is provided a priori before run-time with the transmission scheme of the connected nodes, and imposes that at a given instant only one node is capable of transmitting to the remaining nodes according to the

It will be appreciated that the approach illustrated in FIG. 2 suffers from use of a star coupler. In many environments it is not feasible to run a communication channel from every node to the star coupler for economical reasons. In addition, the star coupler represents a single point of failure in the system that has a higher probability of failure compared to a passive component such as a bus since it contains a significant number of active components, such as, for example, a microcontroller.

Referring now to FIG. 3, a network 300 incorporating the present invention contains four nodes N1-N4 and three components D1-D3, termed 'communication diodes', which will be explained in more detail below. The communication diode D1 is connected between the node N1 and the communication medium; the communication diode D2 is connected between the node N1 and the communication medium; and the communication diode D3 is connected between the node N3 & N4 and the communication medium. As will be explained in greater detail below, the communication diodes D1-D3 are controlled to enable/disable their respective nodes from accessing the communication medium.

25

30

A key virtue of the invention is its versatility: the communication diode can be deployed in a multitude of ways - it can not only be used to protect a shared communication media like a bus or a star from a node (illustrated in FIG. 3 with D1 and D2) but it can also protect subnets from subnets (illustrated with D3). It is

also possible to use the communication diode to physically move the node from the bus connection as shown for node N1 and node N2. In addition it is possible to operate the diodes in a unidirectional or bi-directional way.

5

10

FIG. 4 shows a communication network 400 where a separate disjoint control network 410 interconnects three nodes N11-N13, which are coupled to communication medium 420 by respective communication diodes D11-D13.

FIG. 5 illustrates the structure of a communication diode, which may be used as the communication diodes D1-D3 and D11-D13. The communication diode 500 has two communication channel interfaces 510 & 520 (having interface amplifiers 530, 540, 550 & 560), switches 570 & 580 and control logic 590. The control logic 590 contains the rule base used to control the switches. The rules may range from a pure time-access pattern to more 20 sophisticated rules that consider and/or are controlled by packets that are picked up by the communication diode at an interface 595. Optional connections (dashed lines) indicate that it is also possible to have the communication diode communicate with other devices, for 25 example, for maintenance purposes. The optional interface from the control logic can be used, for example, to connect the communication diode to a separate control network (not shown).

30 It will be understood that the networks 300 and 400 provide a dependable communication in the event of node

error/failure by enforcing fail-silence of the node in the time domain. It will be understood that these networks provide isolation of a faulty processing node and/or subnets within an embedded distributed real-time communication system such as, for example, in automotive by-wire applications ('FlexRay', 'Time-Triggered Protocol' - TTP) under consideration of spatial proximity faults.

- 10 In summary, it will be appreciated that the networks 300 and 400 provide:
 - spatial separation between processing node and guards
- they may be placed within the network line (allowing eavesdropping)
 - they require no control signals from processing nodes (since they are controlled by a separate control network among guards)
 - they may be controlled not only by time but also by commands embedded in frames.

20

It will further be understood that the communication diode 500 may conveniently be fabricated in integrated circuit form (not shown), and may be inserted as desired at one or more points in a network to provide the advantageous functionality described above.

Claims

- 1. A communication network comprising:
 - a plurality of nodes;
- between the plurality of nodes; and

 communication diode means for controlledly
 enabling/disabling access of at least one of the
 plurality of nodes to the communication medium by
 control means external to the at least one of the
 plurality of nodes.
 - 2. The communication network of claim 1, wherein the communication diode means comprises:
- channel interface means for interfacing between the diode means and a communication channel; and switch means for enabling/disabling signals on the channel.
- 20 3. The communication network of claim 2, wherein the switch means comprises:

first direction switch means for enabling/disabling signals in a first direction on the channel; and

- second direction switch means for enabling/disabling signals in a second direction on the channel opposite the first direction.
- The communication network of claim 1, 2 or 3,
 wherein the communication diode means is arranged to be

controlled by time-based control signals from an external control network.

- 5. The communication network of claim 1, 2 or 3,
 wherein the communication diode means is arranged to be controlled by frame-based control signals from an external control network.
- 6. The communication network of any preceding claim,
 10 wherein the network comprises a distributed computer system.
 - 7. The communication network of any preceding claim, wherein the network is arranged for real-time communication.
 - 8. The communication network of any preceding claim, wherein the network is arranged for use in an automotive application.
 - 9. An arrangement for use in a communication network having a plurality of nodes, and a communication medium for communicating between the plurality of nodes, the arrangement comprising:
- communication diode means for controlledly enabling/disabling access of at least one of the plurality of nodes to the communication medium by control means external to the at least one of the plurality of nodes.

30

15

20

 $V_{1}^{\prime}=\mathbb{Z}$

10. The arrangement of claim 9, wherein the communication diode means comprises:

5

20

25

channel interface means for interfacing between the diode means and a communication channel; and switch means for enabling/disabling signals on the channel.

- 11. The arrangement of claim 10, wherein the switch means comprises:
- first direction switch means for enabling/disabling signals in a first direction on the channel; and

second direction switch means for enabling/disabling signals in a second direction on the channel opposite the first direction.

- 12. The arrangement of claim 9, 10 or 11, wherein the communication diode means is arranged to be controlled by time-based control signals from an external control network.
- 13. The arrangement of claim 9, 10 or 11, wherein the communication diode means is arranged to be controlled by frame-based control signals from an external control network.
- 14. The arrangement of any one of claims 9-13, wherein the network comprises a distributed computer system.
- 30 15. The arrangement of any one of claims 9-14, wherein the network is arranged for real-time communication.

16. The arrangement of any one of claims 9-15, wherein the network is arranged for use in an automotive application.

5

- 17. An integrated circuit comprising the arrangement of any one of claims 9-16.
- 18. A communication network substantially as
 10 hereinbefore described with reference to FIGS. 3-5 of the
 accompanying drawings.
 - 19. An arrangement, for use in a communication network, substantially as hereinbefore described with reference to FIGS. 3-5 of the accompanying drawings.
 - 20. An integrated circuit substantially as hereinbefore described with reference to FIG. 3-5 of the accompanying drawings.

20







Application No:

GB 0206737.9

Claims searched: 1 to 20

Examiner:

Brian Hughes

Date of search: 11 November 2002

Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.T): H4P (PPD, PPG, PPHB, PPJB, PPK)

Int Cl (Ed.7): H04L 12/40, 12/403, 12/407, 12/42, 12/423, 12/ 427, 12/44, 29/14; G06F

11/20

Other: Online: EPODOC, WPI, JAPIO

Documents considered to be relevant:

Category	Identity of document and relevant passage		Relevant to claims
X, E	WO 02/47334 A1	(EMULEX CORPORATION) Page 3 line 21 to page 11 line 4	1, 2, 4, 6, 9, 10, 12, 14, 18 and 19
X	US 4530085	(HAMADA et al) Column 2 line 57 to column 5 line 41	1, 5, 9, 11, 13, 18 and 19

& Member of the same patent family

- A Document indicating technological background and/or state of the art.
- P Document published on or after the declared priority date but before the filing date of this invention.
- E Patent document published on or after, but with priority date earlier than, the filing date of this application.

X Document indicating lack of novelty or inventive step

Document indicating lack of inventive step if combined with one or more other documents of same category.